A WIDE SPEED RANGE SENSORLESS CONTROL TECHNIQUE OF BRUSHLESS DC MOTORS FOR ELECTRIC PROPULSORS

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Key words: permanent-magnet brushless DC motor, sensorless, electric propulsor, thruster.

ABSTRACT

This paper presents a sensorless drive technique for permanent-magnet brushless DC motors applied to electric propulsors of small ships or underwater vehicles. Propulsors enveloping electric motors (e.g., POD or subsea thrusters) have been increasingly applied due to their advantages of excellent maneuverability, high efficiency and low maintenance costs. To secure reliable operation under water, sensorless control is considered as an appropriate technique. However, conventional sensorless techniques often suffer problems of narrow speed range and low efficiency, particularly when the propulsors sustain heavy load at high speed operation. Hence, a sensorless technique with a wide speed range is developed in this paper using an enhanced commutation method. A brushless DC motor is first used to demonstrate the effectiveness of the developed technique. An electric thruster is further tested, where the results show that the efficiency matches that of the common Hall element commutation approach.

I. INTRODUCTION

Electronically controlled motors are widely used in various applications demanding speed regulation and control. Permanent-magnet (PM) brushless DC (BLDC) motors are a typical example with advantages of high efficiency, high power density and maintenance-free operation [10, 11]. However, in some applications with limited space, high environmental temperature or severe vibrations, use of Hall-effect sensors for rotor position detection may encounter potential hazards or problems [4, 5]. For example, high temperature can affect the characteristics or cause failure to sensors while insufficient space would not be able to accommodate sensors. Other conditions such as severe vibration or in-water operation may involve a risk of sensor destruction. Therefore, sensorless control is considered as an appropriate technique for such applications.

Due to the demand of environmental protection and the increasing energy crisis, electric motors have been recognized as a suitable device for propulsions of vehicles or ships [1]. However, in these applications, the operating environment is harsh due to severe humidity, vibration, or high temperature. This leads to a serious challenge to the reliability of commutation for BLDC motors [1, 4, 5]. In the case of underwater vehicles, sensorless control can reduce not only the number of external wiring/connections between the motor and driver but also the maintenance requirement caused by vibration [1, 18]. A typical remotely operated vehicle is equipped with four to six thrusters and a significant amount of cables and connectors can be reduced with sensorless control. This will improve the system reliability and reduce cost. Some thrusters for underwater vehicles are designed to allow water to flow inside the motors, such that it is not appropriate for Hall-effect sensors to be installed [9]. This accounts for the necessity of sensorless control.

There are studies reporting sensorless control techniques for BLDC motors [1, 3-7, 12-19]. Some of them are based on detection of zero crossing points (ZCPs) of back electromotive force (EMF) or terminal voltages [6, 12, 13, 16]. The ZCP techniques often require the voltage of the neutral point or virtual neutral point, which are used to compare with terminal voltages for commutation. Iizuka et al. [12] proposed a sensorless approach that compares filtered terminal voltages with that of virtual neutral points for commutation. In this case, a phase shift to the ZCPs is required for correct commutation. Chen and Liaw [6] compared terminal voltages with virtual neutral points to obtain the three phase voltages and thus the line-to-line voltages, whose ZCPs are used as the commutation points. However, neutral or virtual neutral points often contain high-frequency noise [16] and windings of common motors may have no lead-outs for neutral points. To avoid this problem, Damodharan and Vasudevan [7] indirectly obtained back-EMF ZCPs with a series of signal subtractions and manipulation. However, a thirty electric degree phase shift is still required for correct commutation, and the entire circuitry is complex. Chen and Cheng [3-5] directly compared the fil-

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tered average terminal voltages to obtain the required signals for commutation. For the above methods, the inclusion of low-pass filters (LPFs) may solve the noise problem due to pulse-width modulation (PWM) switching but will limit the speed range (because of the phase lag that affects the commutation accuracy). In addition, little literature discussed the speed range that can be achieved under heavy load conditions.

In this paper, a sensorless control technique is developed based on terminal voltage comparison for commutation of BLDC motors applied to electric propulsors. From an electric motor point of view, the typical load characteristics of a propeller can be illustrated with the curves shown in Fig. 1. It can be seen that the torque is approximately proportional to the propeller rotational speed. As the inlet water velocity to the propeller or the vehicle speed increases, the torque required for the same rotational speed decreases [8]. This indicates that the torque for low speed operation is very low, which is considered as an advantage for sensorless control because its start-up is often a problem to conquer. On the other hand, the dramatically increasing torque for high speed reveals a severe challenge to sensorless technique due to its poor performance and efficiency at high torque and high current. This will limit the speed range. Therefore, this paper aims to improve the sensorless performance and efficiency of propulsors under heavy load for high speed operation, as well as to solve the problem at low speed and start-up due to weak signals. Thus, the speed range can be improved for application of sensorless control to electric propulsors.

The remainder of this paper is organized as follows. Section II will analyze the commutation approach for sensorless control, and Section III describes the detail of the proposed technique. The system architecture is illustrated in Section IV while the experimental results for both off-water and in-water tests that verify the proposed method are presented in Section V. Finally, a conclusion is given in Section VI.

### II. ANALYSIS OF COMMUTATION

The equivalent circuit for a three-phase BLDC motor with its power stage is shown in Fig. 2, where $V_{dc}$ is the DC bus voltage, $G_{1d}/G_{2d}/G_{3d}/G_{1l}/G_{2l}/G_{3l}$ are the MOSFETs forming the power stage, $n$ is the neutral point for the wye connection, $R_a/R_b/R_c$ and $L_a/L_b/L_c$ are the motor phase resistances and inductances, respectively, $e_a/e_b/e_c$ are the back EMFs and $i_a/i_b/i_c$ are the phase currents. Ideally, the zero crossings of back EMFs are the most suitable signals for commutation because they have a firm relationship with rotor position. However, the circuits to acquire the back EMFs are often complex. Also, the ZCPs of back EMFs require a 30°E phase shifter, which also adds complexity to the circuits. Therefore, use of the easily-acquired terminal voltages is considered as a better solution [3, 5]. To develop the proposed technique analysis for conventional sensorless approaches first analyzed as follows.

As shown in Fig. 2, assuming the resistances of the three phases are identical, and the rotor inductances do not vary with position, thus $R_a = R_b = R_c = R$, $L_a = L_b = L_c = L$, and the mutual inductances, denoted $L_{Ml}$, are the same. The terminal voltages of the three phases can then be given as:

\[ V_{ag} = R_i a + \frac{d}{dt}i_a + e_a + V_{ng} \]  
\[ V_{bg} = R_i b + \frac{d}{dt}i_b + e_b + V_{ng} \]  
\[ V_{cg} = R_i c + \frac{d}{dt}i_c + e_c + V_{ng} \]

where $L = L_a - L_{Ml}$ and $V_{ng}$ is the voltage of the neutral point referring to the ground. Thus, the line-to-line voltages, $V_{ab}$, $V_{bc}$, $V_{cb}$, can be expressed as:
\[ V_{ab} = R(i_a - i_b) + L \frac{d}{dt}(i_a - i_b) + (e_a - e_b) \]  
\[ V_{bc} = R(i_b - i_c) + L \frac{d}{dt}(i_b - i_c) + (e_b - e_c) \]  
\[ V_{ca} = R(i_c - i_a) + L \frac{d}{dt}(i_c - i_a) + (e_c - e_a) \]  

Equations (4)-(6) describe the relationship of the line-to-line voltages and the back EMFs. For common BLDC drives (i.e., the six-step commutation), the phase currents are assumed to be constant in an excitation step at steady state so that the phase difference and inductor voltages can be neglected. In PM motors, the excitation will be applied in phase with the back EMF waveforms to produce maximum and constant torque. Since back EMFs correspond with the rotor positions, it implies that, according to (4)-(6), the relationships of the line-to-line voltages to the rotor positions can also be identified.

Figure 3 shows the ideal voltages and signals for the BLDC motors running in a certain direction, including terminal voltage, phase voltages, phase currents, sample line-to-line voltages with their subtractions, and Hall sensor signals, etc. As can be seen, finding the ZCPs of the line-to-line voltages can determine the commutation signals without the voltages of the neutral or virtual neutral points. For instance, the ZCP of \( V_{ab} \) can determine the estimated commutation signal \( H_{E2} \) (\( V_{ca} \) in Fig. 3 is the operating voltage of Hall ICs). This is one possible approach for sensorless control although LPFs are needed for the terminal voltages prior to calculation of the line-to-line voltages. Thus, commutation accuracy can be affected by the possible phase lag. Also, subtractors and zero detection circuits are further required for acquiring estimated commutation signals. Alternatively, indirect back EMFs can be determined by subtractions of the line-to-line voltages in (4)-(6), and this will give [7]:

\[ V_{bc} - V_{ca} = -2e_c \]  
\[ V_{ab} - V_{bc} = -2e_a \]  
\[ V_{ca} - V_{ab} = -2e_a \]  

where for any steps in Fig. 3, the voltages on the resistors of the two excited phases will cancel each other so as the two inductors (e.g., in the zone of \( \theta_e = 120^\circ \)E to \( 180^\circ \)E in Fig. 3, \( i_a = -i \) and \( i_b = 0 \), such that the subtraction of \( V_{ab} \) in (4) and \( V_{bc} \) in (5) makes \(-2e_c \) in (8)). This can indirectly acquire the back EMFs but a complicated process and complex circuitry is needed. A phase shift is also necessary for correct commutation, as shown in Fig. 3. Moreover, the line-to-line voltage lags the phase voltages (\( V_{am}, V_{bn}, \) and \( V_{cn} \)) by \( 30^\circ \)E (e.g., \( V_{ab} \) lags \( V_{ab} \)) and for the approaches using phase voltages for commutation [12], a phase shift is again required.

By observing the terminal voltages/Hall signals in Fig. 3, an alternative solution can be discovered. At \( 180^\circ \)E where the comparison between the terminal voltages of phases \( A \) and \( B \) is made, i.e., \( V_{bg} \geq V_{ag} \) (neglecting the PWM effect), the estimated commutation signal \( H_{E2} \) becomes high and this indicates a commutation point. This also applies to the other two cases: comparison between phases \( B \) and \( C \), and that between phases \( C \) and \( A \). Therefore, the circuit shown in Fig. 4 can be used to generate commutation signals (modified from [4]). As can be seen, the circuit consists of a LPF (with a voltage divider) for each phase and three comparators. The LPFs are used to reject the noise on the average terminal voltages caused by PWM switching if the duty is less than 100%. The three filtered average terminal voltages \( A_{avg}, B_{avg}, C_{avg} \) are compared pair by pair to generate the required estimated commutation signals that can be used to replace the Hall signals, as indicated in Fig. 5. These signals, denoted \( H_{E1}, H_{E2}, \) and \( H_{E3} \), are hereafter called “estimated commutation signals” to distinguish from the actual Hall signals. This approach takes advantage of using only comparators to compare the terminal voltages, and the commutation signals can be obtained.

Although this approach has been investigated in [4, 5], the effect of the LPFs and the speed range limitation has not been discussed. These problems are critical to propulsion applica-
BLDCM

A

B

C

Aavg

Bavg

Cavg

Aavg - Cavg

Bavg - Aavg

Cavg - Bavg

HE

HE

HE

Fig. 4. Circuit of commutation signal generation (modified from [4]).

Fig. 5. Commutation signals generated by terminal voltage comparison.

It is noted that the BLDC is initially driven as a synchronous motor because at the start-up, the magnitude of the estimated signals is zero or inadequate for commutation. To enable the commutation using the estimated signals, the motor should achieve a certain speed level. Moreover, at the start-up, the noise added to the already-weak signals will cause problems for switching to the commutation mode. Thus, a higher threshold speed is needed for the motor being switched to the commutation mode, and consequently the motor performance decreases. The proposed approach is capable of applying commutation at low speed by the regulable cut-off frequency.

III. FILTER DESIGN FOR COMMUTATION

The proposed cut-off frequency is illustrated in this section. As shown in Fig. 6, each filter has two capacitors, one of which is connected to a transistor controlled by a microcontroller. The microcontroller changes the cut-off frequency according to the motor speed. This configuration provides two cut-off frequencies in each filter so that a wide range of noise rejection can be achieved, and the phase lag can be reduced. The LPF with a lower cut-off frequency is used for the low terminal voltage at low speed to reject all sorts of noise. This improves the signal quality and makes it easy for start-up. When the motor speed reaches a threshold, the LPF is switched to a higher cut-off frequency by disconnecting one of the capacitors to reduce the phase lag and expand the speed range. This implies that, at low speed operation, a low cut-off frequency is needed while at high speed a high cut-off frequency is required. The two cut-off frequencies should be carefully designed.

In Fig. 6, the capacitors $C_{1a}$, $C_{1b}$ and $C_{1c}$ are controlled to change the cut-off frequencies to fit different speed range. At low speed, the three capacitors are connected for the larger capacitance and lower cut-off frequency, which is denoted $f_{cut,1}$. As mentioned, the BLDC motor is initially driven as a synchronous motor from standstill until the estimated commutation signals from the comparators can be used for commutation. The period of the synchronous motor mode should ideally be as short as possible to improve the motor starting performance and controllability. At the threshold speed of switching from the synchronous model to the commutation mode, the
Table 1. Motor specifications.

<table>
<thead>
<tr>
<th>Specification</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>Rotor poles</td>
<td>8</td>
</tr>
<tr>
<td>Rated output power</td>
<td>750 (W)</td>
</tr>
<tr>
<td>No load speed</td>
<td>1650 (rpm)</td>
</tr>
<tr>
<td>Rated voltage</td>
<td>36 (V)</td>
</tr>
<tr>
<td>Rated speed</td>
<td>1200 (rpm)</td>
</tr>
<tr>
<td>Rated torque</td>
<td>6 (N-m)</td>
</tr>
</tbody>
</table>

mode-switching frequency, \( f_{e,\text{start}} \) should be lower than but close to \( f_{\text{cut},1} \) to ensure good signal quality. At high speed, the three capacitors \( C_{1A}, C_{1B}, C_{1C} \) are disconnected for a higher cut-off frequency \( f_{\text{cut},2} \). The frequencies can be determined by the following equations:

\[
\begin{align*}
    f_{\text{cut},1} &= \frac{1}{2\pi R_m (C_{1A} + C_{2A})} \\
    f_{\text{cut},2} &= \frac{1}{2\pi R_m C_{2A}}
\end{align*}
\]

where the “\( x \)” in \( C_{1x} \) and \( C_{2x} \) represents A, B or C, and \( R_m = R_{d1}R_{d2}/(R_{d1} + R_{d2}) \). The relationship for all the related frequencies can be given as follows:

\[
    f_{e,\text{start}} < f_{\text{cut},1} < f_{e,\text{run}} < f_{\text{cut},2} < f_{\text{PWM}}
\]

where \( f_{\text{PWM}} \) represents the switching frequency of the PWM, and \( f_{e,\text{run}} \) is the highest operation frequencies when the motor runs at the no load speed in the BLDC commutation mode. The cut-off frequency \( f_{\text{cut},2} \), which is significantly higher than the motor highest frequency (at no load speed), is responsible for rejecting PWM noise without causing severe phase lag.

For the investigated motor (Fig. 7) with the specifications shown in Table 1, the no-load speed is 1650 rpm and thus the frequency \( f_{e,\text{run}} \) is 110 Hz for an 8 pole configuration. As the Bode plot for the two filters shown in Fig. 8, the 20 Hz \( f_{\text{cut},1} \) rejects noises for the terminal voltages at 8 Hz \( f_{e,\text{start}} \). The 390 Hz \( f_{\text{cut},2} \) allows the motor runs at the no load speed (110 Hz), by rejecting the 18 kHz \( f_{\text{PWM}} \) and causing little phase lag. The speed range of the motors is expected to be effectively improved with this design.

IV. SYSTEM ARCHITECTURE

To implement the proposed sensorless technique, a 16-bit microcontroller, dsPIC, produce by Microchip is used to work with the developed circuit (Figs. 6 and 7) and the MOSFET-equipped power stage to produce the estimated commutation signals and drive the BLDC motor. The microcontroller is programmed to deal with the start-up process, commutation, cut-off frequency regulation, etc. For accurate commutation, a function called “digital compensation” is also included, which is used to compensate any phase offset caused to the estimated signals due to various speed or load conditions. Also, another technique called “phase advance control” is applied in the developed algorithm to further extend the speed range. These will be detailed in the following.

1. Start-up Process

The entire sensorless system is shown in Fig. 9. The direction of rotation is first determined by the command given to the sensorless drive for speed control. The direction of rotation indicates a corresponding excitation sequence for the synchronous motor mode in the start-up process. Following this, the start-up process is activated until the motor switches to the commutation mode (“closed-loop commutation” in Fig. 9). During the start-up, the synchronous frequency
will gradually increase until the threshold frequency $f_{e_{\text{start}}}$ is reached. At $f_{e_{\text{start}}}$, the commutation signal estimated by the developed circuit should have sufficient magnitude and quality for mode switching. The microcontroller then switches the drive system into the commutation mode using the estimated signals generated by the circuits (Fig. 7). In addition to signal magnitude and quality, the mode-switching frequency $f_{e_{\text{start}}}$ also depends on the load condition, i.e., the load torque at low speed. In this study, the thruster endures a light load at low speed, as shown in Fig. 1, and thereby the mode-switching frequency is determined.

2. Phase Compensation and Phase Advance Control

1) Digital Phase Compensation

Although there are two cut-off frequencies used in the proposed method for wide speed range, the phase lag due to the LPFs is inevitable. In this paper, the phase lag is compensated by digital compensation. Conventional approaches applied constant phase compensation [12], which is not able to compensate in accordance with speed. Some research used look-up table [19] to vary the compensation with speed but a large table should be built if the compensation is to be made precise. Therefore, this paper develops a method that can continuously modify the compensation as speed varies.

As shown in Fig. 10, the commutation signal $H_{E1}$ is assumed to have a phase lag $\theta_1$ due to LPFs. If the motor commutates according to this lagging signal, the motor performance and efficiency will drop. Therefore, for precise commutation, proper compensation should be made. In the developed technique, if there is any phase delay caused to the estimated commutation signal (e.g., $H_{E1}$) by the developed circuits, as shown in Fig. 10, the commutation will not be immediately made to the motor. Instead, the microcontroller will await a delay of $\theta_2$ before performing commutation. The $H_{E1}$ in Fig. 10 is only a virtual signal dealt with by the control algorithm. To use this virtual signal (with a 60°E delay to the original ideal signal) for commutation, indicates that a different commutation table should be applied for correct commutation. It is noted that the sum of $\theta_1$ and $\theta_2$ is 60°E, i.e., one of the six steps of commutation in an electric cycle. Instinctively, it looks like a 60°E delay is caused to commutation, but by applying the new table, the commutation can be made precise.

Ignoring the higher-order harmonics in the trapezoidal terminal voltages, the phase lags caused by the LPF for the start-up mode (before switched to commutation mode) can be given as follows by taking Phase A in Fig. 6 as an example:

$$
\theta_1 = \tan^{-1}(\omega(C_{1A} + C_{2A})R_m)
$$

(13)

where $\omega$ is the angular velocity of the estimated commutation signals using the proposed commutation circuit ($\omega$ can also be used to determine the rotor speed of the 8-pole motor), and $R_m$ is as defined in (10) and (11). The other symbols can refer to Fig. 6. Similarly, for the commutation mode, where the capacitor $C_{1A}$ is disconnected, the phase lag becomes

$$
\theta_1 = \tan^{-1}(\omega C_{2A}R_m)
$$

(14)

Equations (13) and (14) indicates that a phase lag is expected due to either large capacitance (low speed) or high speed (small capacitance). Consequently, the commutation accuracy is affected, which decreases the motor performance.

As shown in Fig. 10, at a certain motor speed, the compensated angle $\theta_2$ is calculated by subtraction of the phase lag $\theta_1$ and 60°E, i.e.,

$$\theta_2 = 60^\circ - \theta_1$$

(15)

Thus, the time to compensate can be given by
Fig. 11. Signals in sensorless drive: $V_{ag}$ - terminal voltage of phase A, $H$ - switch command to the upper arm MOSFETS, $H_1$ - actual Hall signal, $H_{E1}$ - estimated commutation signal and $D_7$ - signal for commutation delay ("high" means the angle to delay, i.e., $\theta_2$). (1: 20 V/div, 2: 2 V/div; D0, D1, D7: Digital; time: 10 ms/div).

Fig. 12. Phase advance for flux weakening.

\[ t = \theta_2 / \omega \]  
\[ (16) \]

where $\omega$ is as defined in (13). Equation (16) indicates that the compensation time is determined according to the motor speed. This is included in the control program performed on a microcontroller.

Figure 11 shows the actual signals measured from the BLDC motor at a certain speed using the developed commutation circuit. As can be seen, $\theta_2$ is smaller than 60°E because the estimated signal $H_{E1}$ lags the actual Hall signal $H_1$. This needs to be compensated. In the following experimental studies, the digital compensation has been applied to all the cases.

2) Phase Advance Control

In addition to the precise commutation, the digital compensation can also perform phase advance control, which allows the commutation applied in advance. When digital compensation is made to perform the commutation in advance, the excitation current will also be applied in advance [2, 17]. This will produce the flux opposing that produced by the magnet pole and cause flux weakening, as indicated in Fig. 12. The flux weakening effect can raise the speed of the motor above the rated speed. As shown in Fig. 13, controlling $\theta_2$ will control the commutation timing, as well as the current phase. Assuming that no phase lag occurs (i.e., $H_{E1}$ appears at the commutation point, as shown in Fig. 13), to perform current phase advance control, a delay of $\theta_2 < 60°E$ can be used to allow a $\theta_1'$ phase difference with the current leading the back EMF. It is noted that, as described above, another commutation table should be applied.

V. EXPERIMENTAL STUDIES

1. General Sensorless Performance Tests

It is important to investigate the performance under heavy load since this is a major drawback of sensorless techniques. Therefore, two experiments are arranged to test the performance: 1) speed regulation performance under a constant load, and 2) torque performance under a constant speed. The tests on the common BLDC commutation with Hall elements are also conducted and compared with the sensorless technique in terms of drive system efficiency. Moreover, the developed sensorless technique is further applied to a commercial thruster to verify the feasibility.

Loads are applied to the target motor (Fig. 7) via a dynamometer test bench (Fig. 14) after all the circuitry has been tested to be functionally correct. Figures 15 and 16 show the curves of efficiency against speed under constant loads 2 N·m and 3 N·m respectively. As can be seen, for 2 N·m, the efficiency of the proposed method almost matches the case equipped with Hall elements for a wide speed range. For 3 N·m, the efficiency of the proposed sensorless technique becomes slightly lower at some speeds. Figure 17 shows the performance comparison by varying loads up to 6 N·m (rated torque) under the rated speed. As can be seen, for load greater than 3 N·m, the efficiency of the presented technique gradually decreases compared to the drive with Hall elements.

As shown in Fig. 18, Ch1 and Ch2 are respectively the terminal voltage and current of Phase A for the common BLDC
drive with Hall elements under a load of 2 N·m at 1200 rpm. In contrast, Fig. 19 shows the same parameters obtained with the presented technique under the same load condition and speed. As can be seen and expected, the sensorless case requires a slightly larger (2.95% greater) current in comparison with the case with Hall elements.

To sum up, Figs. 15 and 16 demonstrate that the proposed technique can operate the motor in the commutation mode to achieve a speed range from 200 rpm to beyond the rated speed with load applied. The lowest speed at which the commutation using the estimated signals can be applied is 200 rpm. This implies that the synchronous mode is only required from 0 to 200 rpm. In addition, Fig. 17 also presents that the BLDC motor, with the developed sensorless technique, can be operated at the rated torque under the rated speed by a small effi-
Fig. 20. Signals for phase advance control.
(1: 20 V/div; 2: 2 V/div; D0, D1, D7: Digital; time: 5 ms/div)

Fig. 21. Terminal voltages and estimated commutation signal for phase advance on no-load operation.
(1: 20 V/div, 2: 2 V/div; time: 2 ms/div)

Fig. 22. Terminal voltages and estimated commutation signal for phase advance on 2 N-m load operation.
(1: 20 V/div, 2: 2 V/div; time: 2 ms/div)

ciency drop. Therefore, this demonstrates the effectiveness of the presented technique that is capable of operating for a wide speed range under heavy load condition.

2. Phase Advance Control

For the phase advance control, the results are shown in Figs. 20-22. The definitions of the symbols in Fig. 20 are the same as in Fig. 11, and \( \theta_2 \) in Fig. 20 indicates the angle to delay so that the phase advance can be achieved. \( \theta_2 \) can be calculated according to the previous explanation. Figure 21 shows the speed (1800 rpm, 120 Hz) the motor reaches by employing phase advance control under no load condition. This is higher than the original no load speed of the motor, i.e., 1650 rpm.

For the case with 2 N-m applied, as can be seen in Fig. 22, the speed achieved is 1740 rpm with the phase advance control. This is also greater than the speed under the same load for the drive with Hall elements (1500 rpm). The above experiments have confirmed the effectiveness of the developed phase advance control.

3. In-Water Thruster Test

A thruster (Fig. 23(a)) direct driven by another motor is tested in water. This is a commercially available unit without details of the propeller characteristics and motor parameters. During the in-water experiments, the thruster shaft torque cannot be directly measured. Nevertheless, the performance of the developed sensorless technique can still be tested by measuring the input power, which is then compared with that of the drive with Hall elements. For the same propeller run-
VI. CONCLUSION

This paper has developed a sensorless driving technique for BLDC motors and electric propulsors. This technique made use of a low-pass filter with two cut-off frequencies that can be regulated for various speed operations. The higher cut-off frequency was designed for high speed to reduce phase lag, and the lower one was used for start-up to reject the noise. From the experiments, with loads applied, the developed technique was capable of increasing the speed beyond the rated speed. The BLDC motor can be operated in the commutation mode from a 200 rpm low speed using the estimated signals acquired by the developed circuits. For the entire speed range, the proposed technique demonstrated high efficiency even at high torque, high speed condition. Thus, the performance of the developed technique can match that of drives with Hall elements. The tests for the electric propulsors in water have also shown that the proposed method can have a decent efficiency and performance compared to that of Hall-element commutation.

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Fig. 24. In-water test results: (a) no flow speed, (b) 0.5 m/s flow speed and (c) 0.77 m/s flow speed.


